

Micron Candidate Guide

Micron is looking for the brightest minds to join our international team of innovators. Match your education and experience to see what jobs might be a fit for you.



Micron Department	Job Title(s)	Education	Degree	Required Coursework	Optional Coursework	Job Location(s)
SSD Engineering	<ul style="list-style-type: none"> • ASIC Digital Design Engineer • ASIC Verification Engineer 	<ul style="list-style-type: none"> • BS • MS 	<ul style="list-style-type: none"> • Electrical Engineering • Computer Engineering 	<ul style="list-style-type: none"> • Digital Logic Design • VLSI Design • Verilog/VHDL/ SystemVerilog • Computer Architecture 	<ul style="list-style-type: none"> • Electrical Engineering • Computer Science 	<ul style="list-style-type: none"> • Boise, ID • Longmont, CO • Milpitas, CA
DRAM Engineering	<ul style="list-style-type: none"> • Product Engineer • Design Engineer 	<ul style="list-style-type: none"> • BS • MS 	<ul style="list-style-type: none"> • Electrical Engineering • Computer Engineering 	At least one from: <ul style="list-style-type: none"> • CMOS Logic • Digital Logic • VLSI Design 	<ul style="list-style-type: none"> • C++ • C# • Semiconductor Physics 	<ul style="list-style-type: none"> • Boise, ID
Firmware Development	<ul style="list-style-type: none"> • Firmware Developer • Firmware Architect • Firmware Tester • Firmware Test Architect 	<ul style="list-style-type: none"> • BS • MS • PhD 	<ul style="list-style-type: none"> • Electrical Engineering • Computer Engineering 	<ul style="list-style-type: none"> • Programming projects in C/C++ 	<ul style="list-style-type: none"> • Digital Design • Control Theory • Digital Devices • Real Time Systems • Digital Signal Processing • Computer Architectures 	<ul style="list-style-type: none"> • Boise, ID • Folsom, CA • Longmont, CO • Milpitas, CA • Minneapolis, MN
Information Technology	<ul style="list-style-type: none"> • Software Engineer • Business Analyst • System Manager • Database Administrator • Project Manager 	<ul style="list-style-type: none"> • BS • MS 	<ul style="list-style-type: none"> • Computer Science • Management Information Systems • Computer Information Systems • Electrical Engineering • Computer Engineering 	<ul style="list-style-type: none"> • C#/C++ • SQL 	<ul style="list-style-type: none"> • SQL Server • NET • OOP/OOD • Unix, Linux • Windows • Oracle, MS-SQL • SAP • ABAP • SSIS • SSRS • Perl • Java 	<ul style="list-style-type: none"> • Boise, ID
Micron Technology Virginia (Fab 6)	<ul style="list-style-type: none"> • Fab Engineer • Process Integration Engineer • Equipment Engineer 	<ul style="list-style-type: none"> • BS • MS 	<ul style="list-style-type: none"> • Electrical Engineering • Chemical Engineering • Microelectronics Engineering • Mechanical Engineering • Materials Science • Physics • Chemistry 	<ul style="list-style-type: none"> • Process • Statistics • Manufacturing 	<ul style="list-style-type: none"> • Cleanroom 	<ul style="list-style-type: none"> • Manassas, VA

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Full Time Req #:
 Intern Req #:



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Non-Volatile Engineering Group	<ul style="list-style-type: none"> Product Engineer Design Engineer Verification Engineer Firmware Engineer System Engineer Characterization Engineer Yield Engineer 	<ul style="list-style-type: none"> BS MS PhD 	<ul style="list-style-type: none"> Electrical Engineering Computer Engineering 	At least 1 from: <ul style="list-style-type: none"> CMOS Logic Digital Logic VLSI Design 	<ul style="list-style-type: none"> Programming Language (C++, C#, Java, etc.) Semiconductor Physics Analog Circuits 	<ul style="list-style-type: none"> Boise, ID Folsom, CA Longmont, CO Milpitas, CA
Process R&D Development	<ul style="list-style-type: none"> R&D Process Development Engineer <p>Processes: Photolithography, Dry Etch, Wet Chemistry, Thin Films, CMP, Metrology, Etc.</p>	<ul style="list-style-type: none"> MS PhD 	<ul style="list-style-type: none"> Electrical Engineering Chemical Engineering Materials Science Microelectronics Engineering Physics Chemistry 	<ul style="list-style-type: none"> Device or Processing Research Semi Process Lab Statistics Design of Experiment 	<ul style="list-style-type: none"> SPC Computational Fluid Dynamics Thermo-mechanical Modeling Transport Phenomena Ab-initio (DFT) Modeling 	<ul style="list-style-type: none"> Boise, ID
Process R&D - Integration	<ul style="list-style-type: none"> Process Integration Engineer 	<ul style="list-style-type: none"> BS MS PhD 	<ul style="list-style-type: none"> Electrical Engineering Materials Science Chemical Engineering Physics 	<ul style="list-style-type: none"> Device or Processing Research CMOS 	<ul style="list-style-type: none"> DOE • SPC, JMP Semiconductor Physics Magnetics Device Work Circuit Design 	<ul style="list-style-type: none"> Boise, ID
Mask Technology Center	<ul style="list-style-type: none"> Reticle Manufacturing Engineer Data Science Engineer 	<ul style="list-style-type: none"> BS MS 	<ul style="list-style-type: none"> Materials Science Chemical Engineering Mechanical Engineering Physics Chemistry Mathematics/Statistics 		<ul style="list-style-type: none"> Semiconductor Physics C++, SQL, Java, Python 	<ul style="list-style-type: none"> Boise, ID
Process R&D - Operations	<ul style="list-style-type: none"> Manufacturing Development Engineer Industrial Engineer 	<ul style="list-style-type: none"> BS 	<ul style="list-style-type: none"> Electrical Engineering Chemical Engineering Materials Science Microelectronics Engineering Physics Chemistry 	<ul style="list-style-type: none"> Semi Process Lab PC Skills Device Knowledge Statistics MAT LAB Semiconductor Materials 	<ul style="list-style-type: none"> Statistics Semiconductor Physics Mechanical Understanding of Tools Programming 	<ul style="list-style-type: none"> Boise, ID
Quality	<ul style="list-style-type: none"> Quality Product Reliability Engineer Quality Data Analyst Engineer Manufacturing Quality Engineer 	<ul style="list-style-type: none"> BS MS 	<ul style="list-style-type: none"> Electrical Engineering Mechanical Engineering Computer Engineering 	<ul style="list-style-type: none"> Semiconductor Physics CMOS Logic VLSI Design Statistics • C++ 	<ul style="list-style-type: none"> Manufacturing Project Management 	<ul style="list-style-type: none"> Boise, ID Longmont, CO
Test and Probe	<ul style="list-style-type: none"> Test Engineer 	<ul style="list-style-type: none"> BS 	<ul style="list-style-type: none"> Electrical Engineering Computer Engineering 	<ul style="list-style-type: none"> Electrical/Electronics Digital Logic Programming (prefer C++ and Unix) 		<ul style="list-style-type: none"> Boise, ID

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